

# Abstracts

## A Distributed Broadband Monolithic Frequency Multiplier

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*A.M. Pavio, S.D. Bingham, R.H. Halladay and C.A. Sapahe. "A Distributed Broadband Monolithic Frequency Multiplier." 1988 MTT-S International Microwave Symposium Digest 88.1 (1988 Vol. I [MWSYM]): 503-504.*

A broadband frequency doubler, which employs distributed amplifier techniques, has been designed to operate over several octaves of bandwidth. The new circuit design suppresses the fundamental frequency energy present at the output port while maximizing the second harmonic signal. The design can be realized using monolithic or conventional microwave circuit techniques for use in local oscillator chains.

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